

Refine Search

Search Results -

Terms	Documents
L2 and L3 and L4 and L6 and L7 and L14 and L22 and L23	0

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L24

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Friday, August 04, 2006 [Printable Copy](#) [Create Case](#)

Set
Name **Query**
side by
side

Hit
Count
Set
Name
result set

DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L24	l2 and l3 and l4 and l6 and l7 and l14 and l22 and l23	0	L24
L23	packet	260582	L23
L22	eras* or delet\$	387692	L22
L21	l17 and L20	1	L21
L20	l18 near3 l7	889	L20
L19	l17 and L18	19	L19
L18	reset\$	475798	L18
L17	l7 and L16	30	L17
L16	l6 near3 l14	145	L16
L15	l14 and l2 and l6 and l7	1585	L15
L14	pci or (peripheral adj component adj interconnect)	46975	L14
L13	l6 and l7 and l3 and l12	1	L13
L12	("5835936" "5822251" "5430859" "20030145141" "20030142227" "20030093606")!.ABPN1,NRPN,PN,TBAN,WKU.	12	L12
L11	("5835936" "5822251" "5430859")!.ABPN1,NRPN,PN,TBAN,WKU.	6	L11
L10	l8 and l9	11	L10
L9	l7 with l6	733	L9
L8	l2 and l3 and l5	569	L8

<u>L7</u>	. (flash or eeprom or nonvolatile) adj memory	149060	<u>L7</u>
<u>L6</u>	serial adj2 interfac\$	38469	<u>L6</u>
<u>L5</u>	buffer or cache	916989	<u>L5</u>
<u>L4</u>	(complet\$ or finish\$) adj2 packet	6605	<u>L4</u>
<u>L3</u>	(conver\$ or transform\$ or translat\$) near3 serial near3 parallel near3 data	14055	<u>L3</u>
<u>L2</u>	serial adj bus	46666	<u>L2</u>
<u>L1</u>	(flash or eeprom or nonvolatile) near2 array	9876	<u>L1</u>

END OF SEARCH HISTORY

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Edit an existing query or
compose a new query in the
Search Query Display.

Fri, 4 Aug 2006, 6:12:48 PM EST

Search Query Display

Select a search number (#)
to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

		Results
<u>#1</u>	((serial bus)<in>metadata)	191
<u>#2</u>	(((((serial bus)<in>metadata))<AND>((flash or eeprom) <and> memory<in>metadata)))	3
<u>#3</u>	(pci express<IN>metadata)	38
<u>#4</u>	((flash <or> eeprom) <and> memory<IN>metadata)	3032
<u>#5</u>	(((((flash <or> eeprom) <and> memory<in>metadata))<AND>(pci express<in>metadata)))	0
<u>#6</u>	(serial bus interface<IN>metadata)	16
<u>#7</u>	(((((serial bus interface<in>metadata))<AND>(flash memory<in>metadata)))	0